



# NPN SILICON PLANAR EPITAXIAL DARLINGTON TRANSISTORS



MPSA28 / MPSA29 TO-92

Plastic Package

## ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	MPSA28	MPSA29	UNITS
Collector Emitter Voltage	V <sub>CES</sub>	80	100	V
Collector Base Voltage	V <sub>CBO</sub>	80	100	V
Emitter Base Voltage	V <sub>EBO</sub>	12	V	
Collector Current Continuous	Ι <sub>C</sub>	500	mA	
Power Dissipation at T <sub>a</sub> =25°C	P <sub>D</sub>	625	mW	
Derate Above 25ºC		5.0	1	mW/⁰C
Power Dissipation at T <sub>c</sub> =25 <sup>o</sup> C	PD	1.5		W
Derate Above 25°C		12	mW/ºC	
Operating And Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	- 55 to -	٥C	

### THERMAL CHARACTERISTICS

Junction to Ambient in free air	R <sub>th (j-a)</sub>	200	°C/W		
Junction to Case	R <sub>th (j-c)</sub>	83.3	°C/W		

### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Collector Emitter Voltage	V <sub>CES</sub>	I <sub>C</sub> =100μA, V <sub>BE</sub> =0			
		MPSA2	<b>3</b> 80		V
		MPSA2	<b>)</b> 100		V
Collector Base Voltage	V <sub>CBO</sub>	I <sub>C</sub> =100μA, I <sub>E</sub> =0			
		MPSA2	80		V
		MPSA29	<b>)</b> 100		V
Emitter Base Voltage	V <sub>EBO</sub>	I <sub>E</sub> =10μΑ, I <sub>C</sub> =0	12		V
Collector Cut Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =60V, I <sub>E</sub> =0, <b>MPSA28</b>	•	100	nA
		$V_{CB}$ =80V, $I_{E}$ =0, MPSA29	)	100	nA
Collector Cut Off Current	I <sub>CES</sub>	V <sub>CE</sub> =60V, V <sub>BE</sub> =0, <b>MPSA28</b>	3	500	nA
		V <sub>CE</sub> =80V, V <sub>BE</sub> =0, <b>MPSA2</b>		500	nA
Emitter Cut Off Current	I <sub>EBO</sub>	$V_{EB} = 10V, I_{C} = 0$		100	nA
DC Current Gain	*h <sub>FE</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =10mA	10,000		
		V <sub>CE</sub> =5V, I <sub>C</sub> =100mA	10,000		
Collector Emitter Saturation Voltage	*V <sub>CE (sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0.01mA		1.2	V
		I <sub>C</sub> =100mA, I <sub>B</sub> =0.1mA		1.5	V
Base Emitter On Voltage	*V <sub>BE (on)</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =100mA		2.0	V
Current Gain Bandwidth Product	**f <sub>T</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =5V, f=100MHz	125		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0V, f=1.0 MHz		8.0	pF

\*Pulse test: Pulse Width <300ms, Duty Cycle<2%

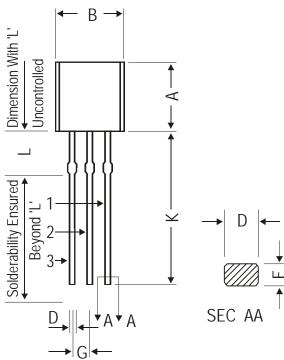
\*\* $f_T = Ih_{fe}I^* f_{test}$ 

MPSA28\_29Rev270607E

## MPSA28 / MPSA29

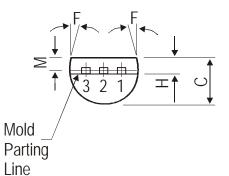
TO-92 Plastic Package

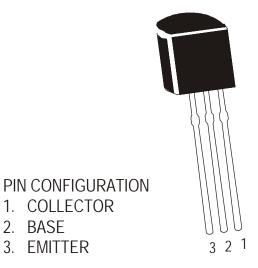




DIM	MIN.	MAX.	
А	4.32	5.33	
В	4.45	5.20	
С	3.18	4.19	
D	0.41	0.55	
E	0.35	0.55	
F	5 DEG		
G	1.14	1.40	
Н	1.20	1.80	
К	12.50	_	
L	1.982	2.082	
М	1.03	1.53	

All dimensions are in mm





The TO-92 Package, Tape and Ammo Pack Drawings are correct as on the date of issue/revision of this Data Sheet.

The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

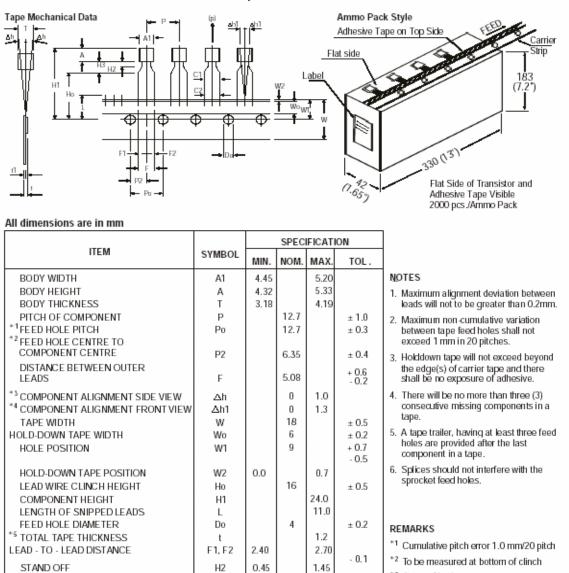
#### **Packing Details**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	5	3" x 7.5" x 7.5"		17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

MPSA28\_29Rev270607E

## TO-92 Plastic Package

#### TO-92 Tape and Ammo Pack



### **Component Disposal Instructions**

CLINCH HEIGHT

LEAD PARALLELISM

PULL - OUT FORCE

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

3.0

0.22

H3

|C1 - C2|

(p)

6N

\*3 At top of body

\*4 At top of body

\*5 t1 0.3 – 0.6 mm

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

MPSA28\_29Rev270607E

TO-92 Plastic Package

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119 email@cdil.com www.cdilsemi.com